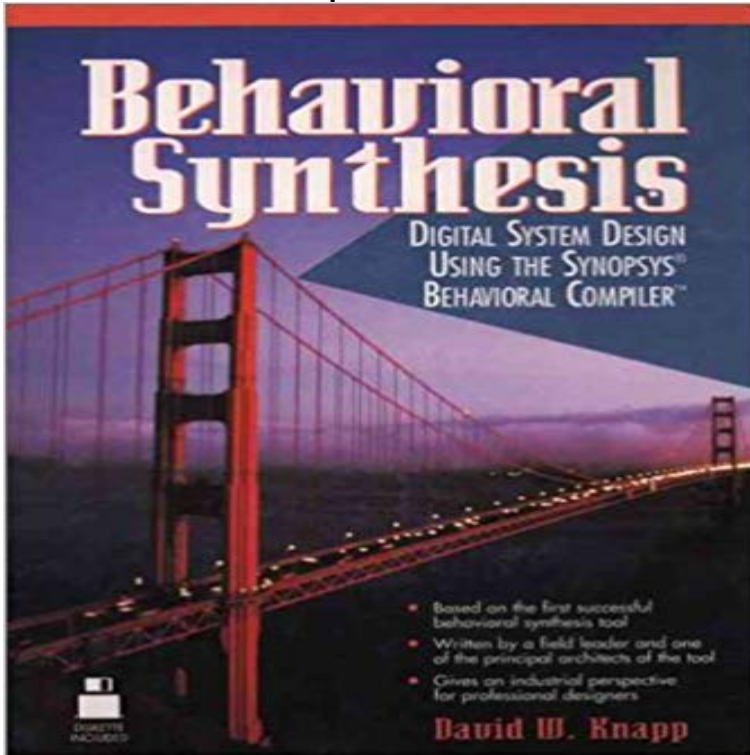


# Behavioral Synthesis: Digital System Design Using the Synopsys Behavioral Compiler



56925-1 Behavioral synthesis is now a reality. After nearly twenty years of academic and industrial research and development, designers are reaping the rewards of this technology, which brings increased productivity, improved design quality, and faster time to market. This book gives a designers-eye view of this exciting new EDA technology. The first six chapters provide a detailed description of what goes on under the hood of an industrial-strength behavioral synthesis product, the Synopsys Behavioral Compiler, with copious notes and tips on how to use this knowledge to get the most out of this powerful new tool. \*

- \* Introduction to the behavioral design flow
- \* Behavioral synthesis representations and processes
- \* Writing HDL descriptions for behavioral synthesis
- \* Managing I/O timing
- \* Behavioral Compiler commands and options
- \* Interpreting error messages

The next five chapters provide detailed case studies, representing a variety of design problems: \*

- \* IIR and FIR filters
- \* Video compression and translation from C to HDLs
- \* Data encryption
- \* Packet routing

The examples are given in both VHDL and Verilog. Machine-readable versions, along with complete synthesis reports, are provided on the accompanying diskette (requires Synopsys Behavioral Compiler to synthesize).

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