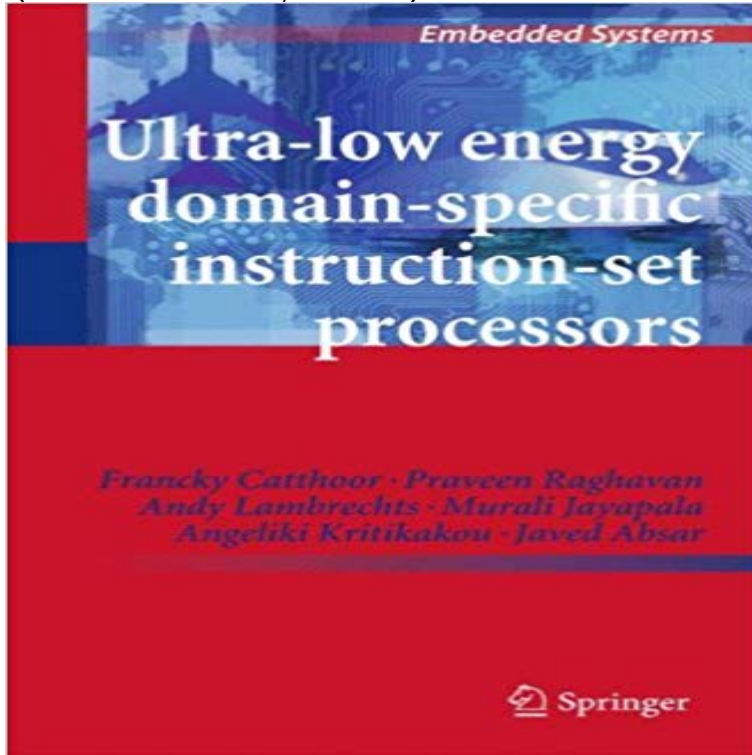


Ultra-Low Energy Domain-Specific Instruction-Set Processors (Embedded Systems)



This book presents a systematic methodology for exploiting word-width information in embedded compilers. It details a technique for a context-driven strength reduction for constant multiplications, including a trade-off with application accuracy requirements.

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